

Notice of Allowability

Application No.

10/812,081

Applicant(s)

TAKIKAWA ET AL.

Examiner

Thomas L Dickey

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to Application filed 03/30/2004.
2. ☒ The allowed claim(s) is/are 1-17.
3. ☒ The drawings filed on 30 March 2004 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some* c) ☐ None of the:
1. ☐ Certified copies of the priority documents have been received.
2. ☒ Certified copies of the priority documents have been received in Application No. 09/547,915.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date 03/30/2004
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____.

Minhloan Tran
Minhloan Tran
Primary Examiner
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REASONS FOR ALLOWANCE

1. The following is an examiner's statement of reasons for allowance:

Claims 1-17 are allowed over the references of record because none of these references disclosed or can be combined to yield the claimed invention such as a semiconductor integrated circuit device comprising a first low noise amplifier into which a radio frequency reception signal of a first frequency band is to be inputted; a second low noise amplifier into which a radio frequency reception signal of a second frequency band is to be inputted, the second frequency band being higher than the first frequency band; a first ground pin electrically connected to said first low noise amplifier, a second ground pin electrically connected to said second low noise amplifier, wherein the first and second low noise amplifiers are monolithically integrated on one semiconductor chip, said chip having four corners, wherein a distance between said first ground pin and a nearest corner of said four corners is shorter than a distance between said second ground pin and a nearest corner of said four corners, as recited in claim 1.

Jensen 6,075,995 discloses a semiconductor integrated circuit device comprising a first low noise amplifier 4 into which a radio frequency reception signal of a first frequency band is to be inputted; a second low noise amplifier 6 into which a radio frequency reception signal of a second frequency band is to be inputted, the second frequency band being higher than the first frequency band; a first ground pin GND_8 electrically connected to said first low noise amplifier 4, a second ground pin GND_7

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electrically connected to said second low noise amplifier 6, wherein a distance between said first ground pin GND_8 and a nearest corner of four corners of a chip is shorter than a distance between said second ground pin GND_7 and a nearest corner of four corners of a chip. Note figures 3 and 4, column 4 lines 60-67, column 5 lines 13-22, column 6 lines 22-29, and 32-33 of Jensen. However, Jensen neither discloses nor suggests that the first and second low noise amplifiers are monolithically integrated on one semiconductor chip.

2. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thomas L Dickey whose telephone number is 571-272-1913. The examiner can normally be reached on Monday-Thursday 8-6.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nathan J Flynn can be reached on 703-308-6601. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published

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applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TLD
09/2004